

General Description

The KSD20N15 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

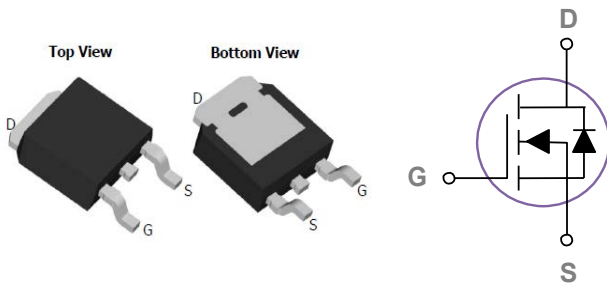
Product Summary

V _{DS} (V)	R _{DS(on)} (mΩ)	I _D (A)
150	63 at V _{GS} = 10 V	20
	78 at V _{GS} = 4.5 V	16

Features

- Trench Power MV MOSFET technology
- Low R_{DS(ON)}
- Optimized for fast-switching applications

TO-252 Pin Configuration



Applications

- Synchronous Rectification in DC/DC and AC/DC Converters
- Industrial and Motor Drive applications

Absolute Maximum Ratings T_c=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _c =25°C)	20	A
	Drain Current – Continuous (T _c =100°C)	15	A
I _{DM}	Drain Current – Pulsed ¹	58	A
P _D	Power Dissipation (T _c =25°C)	63	W
	Power Dissipation (T _c =100°C)	1.2	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	63	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	2.72	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted) Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	150	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=50V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=50V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=10A$	---	63	76	$m\Omega$
		$V_{GS}=4.5V, I_D=8A$	---	78	91	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	2.0	3.3	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	2.0	---	$mV/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=10V, I_S=6A$	---	20	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=50V, V_{GS}=10V, I_D=8A$	---	36	---	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	6	---	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	11	---	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DS}=50V, I_D=8A$	---	10	---	ns
T_r	Rise Time ^{2, 3}		---	34	---	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		$V_{GS}=10V, R_G=10\Omega$	---	55	
T_f	Fall Time ^{2, 3}		---	71	---	
C_{iss}	Input Capacitance	$V_{DS}=50V, V_{GS}=0V, F=1\text{MHz}$	---	1372	---	pF
C_{oss}	Output Capacitance		---	183	---	
C_{riss}	Reverse Transfer Capacitance		---	75	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V, \text{Force Current}$	---	---	20	A
I_{SM}	Pulsed Source Current		---	---	50	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristics (Curves)

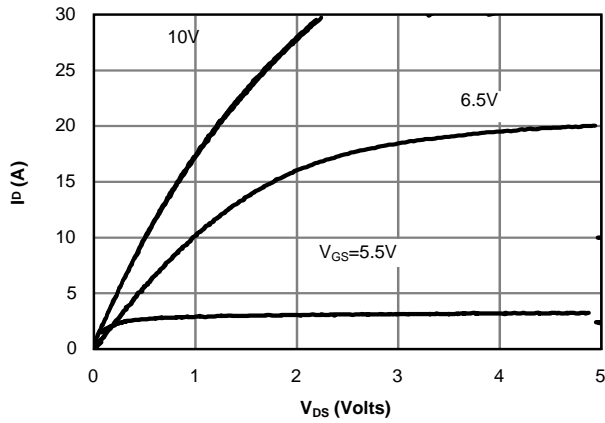


Fig 1: On-Region Characteristics (Note E)

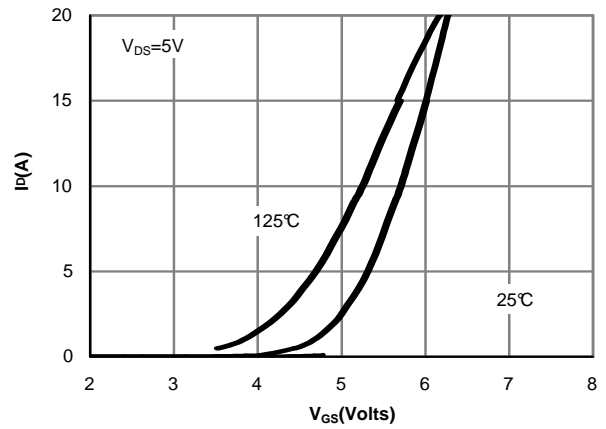


Figure 2: Transfer Characteristics (Note E)

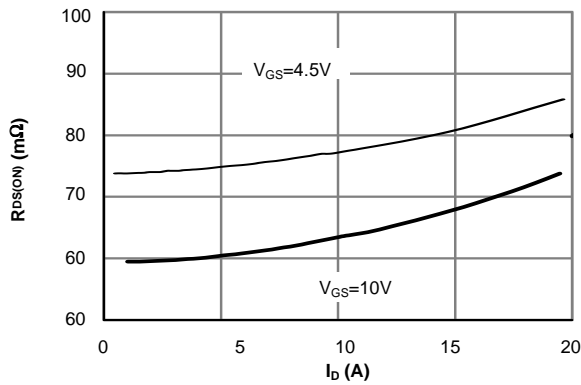


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

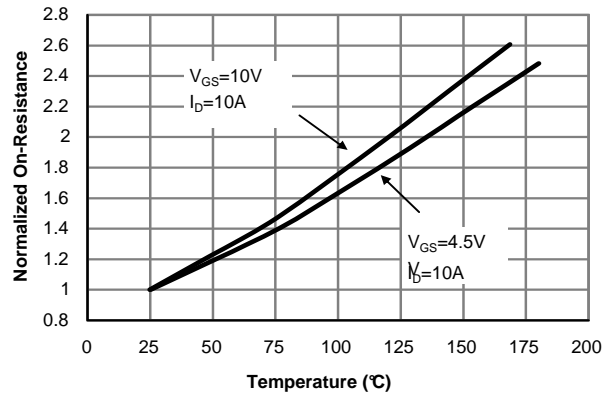


Figure 4: On-Resistance vs. Junction Temperature (Note E)

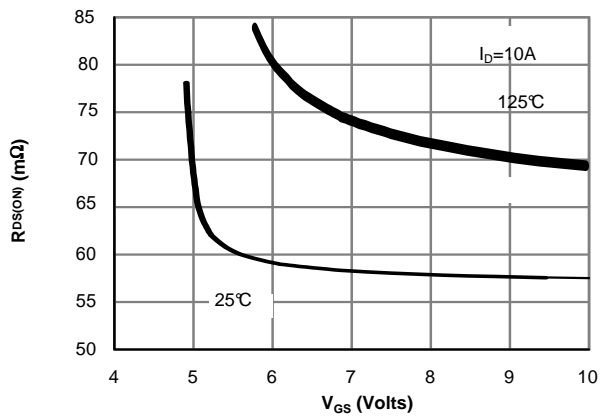


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

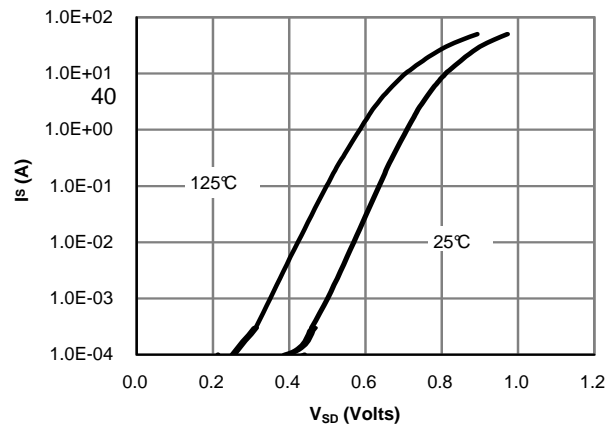


Figure 6: Body-Diode Characteristics (Note E)

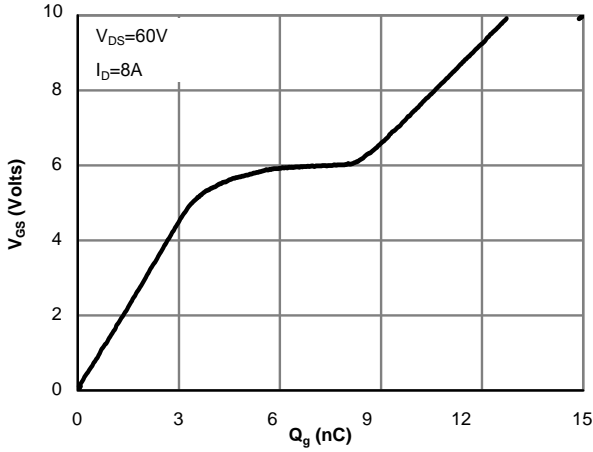


Figure 7: Gate-Charge Characteristics

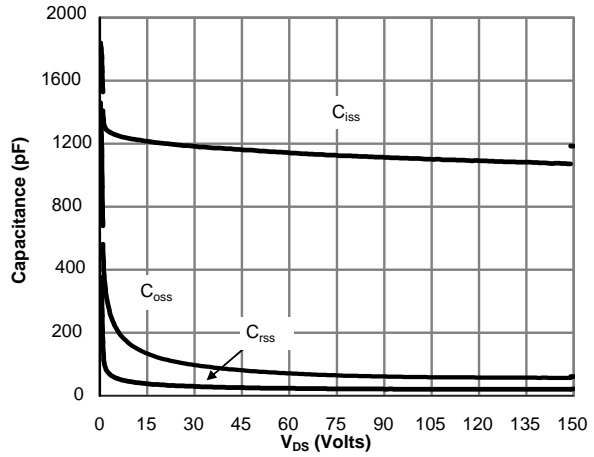


Figure 8: Capacitance Characteristics

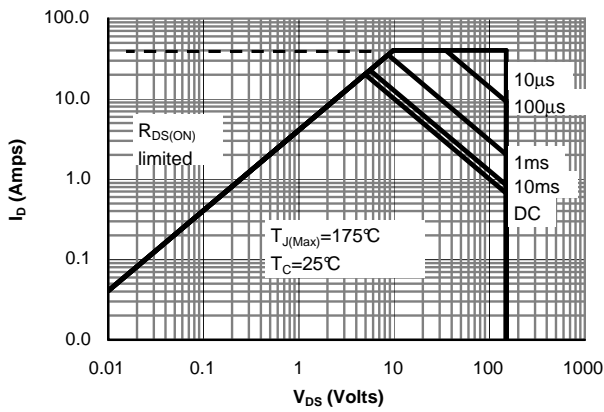


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

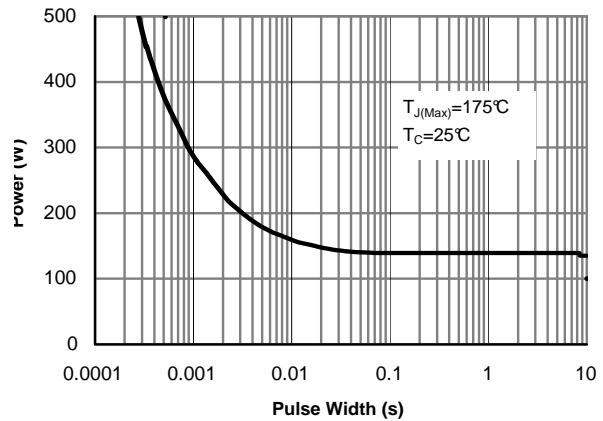


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

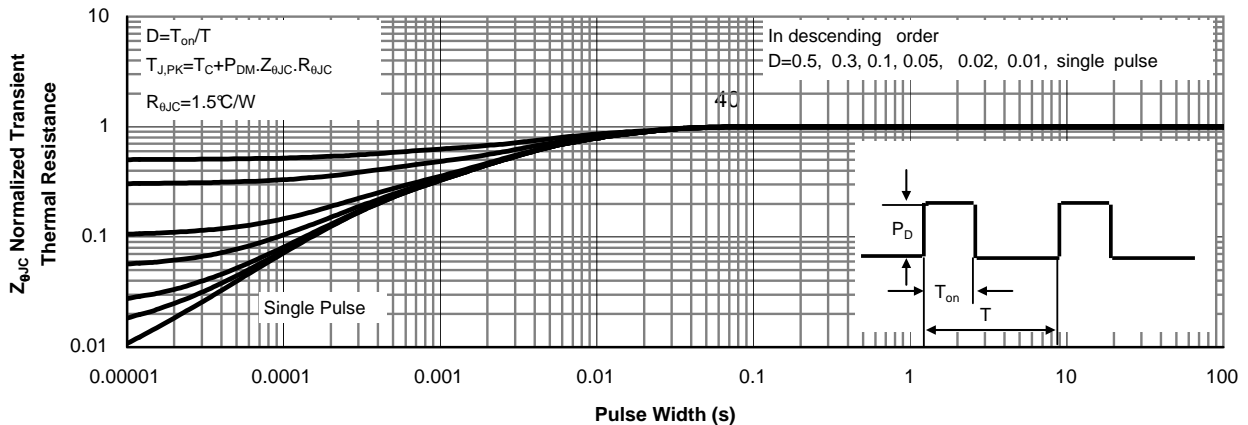


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

Disclaimer

1. Any and all Korsun Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Korsun Semiconductor representative nearest you before using any Korsun Semiconductor products described or contained herein in such applications.
2. Korsun Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Korsun Semiconductor products described or contained herein.
3. Specifications of any and all Korsun Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
4. Korsun Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
5. In the event that any or all Korsun Semiconductor products (including technical data, services described or contained herein) are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
6. No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Korsun Semiconductor CO.,LTD.
7. Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. Korsun Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
8. Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Korsun Semiconductor product that you intend to use.
9. This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.