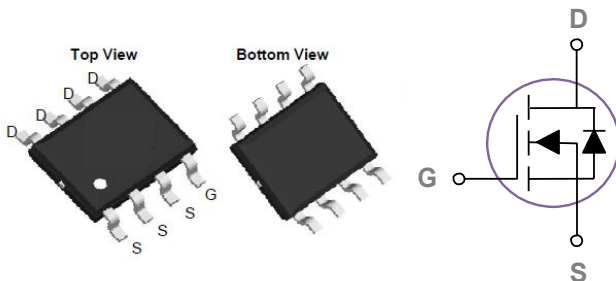


General Description

The KSP4480 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible onresistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

SOP-8 Pin Configuration



Product Summary

V_{DS} (V)	$R_{DS(on)}$ (m Ω)	I_D (A)
40	9.8 at $V_{GS} = 10$ V	12.4
	14.5 at $V_{GS} = 4.5$ V	9.5

Features

- High density cell design for ultra low R_{Dson}
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Applications

- Load switching
- Hard switched and high frequency circuits
- Uninterruptible power supply

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	12.4	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	8.1	A
I_{DM}	Drain Current – Pulsed ¹	44	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	3	W
	Power Dissipation ($T_c=100^\circ\text{C}$)	0.16	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	53	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.7	$^\circ\text{C}/\text{W}$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=35V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=35V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	25	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=10A$	---	9.7	13	$m\Omega$
		$V_{GS}=4.5V, I_D=8A$	---	14.4	19	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	1.5	3	V
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_S=10A$	---	25	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	$V_{DS}=20V, V_{GS}=10V, I_D=10A$	---	33	---	nC
Q_{gs}	Gate-Source Charge		---	5.1	---	
Q_{gd}	Gate-Drain Charge		---	11	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=20V, R_L=2\Omega$ $V_{GS}=10V, R_G=3\Omega$	---	7.3	---	ns
T_r	Rise Time		---	19.2	---	
$T_{d(off)}$	Turn-Off Delay Time		---	36	---	
T_f	Fall Time		---	18	---	
C_{iss}	Input Capacitance	$V_{DS}=20V, V_{GS}=0V, F=1\text{MHz}$	---	1960	---	pF
C_{oss}	Output Capacitance		---	230	---	
C_{rss}	Reverse Transfer Capacitance		---	205	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	12.4	A
I_{SM}	Pulsed Source Current		---	---	30	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics (Curves)

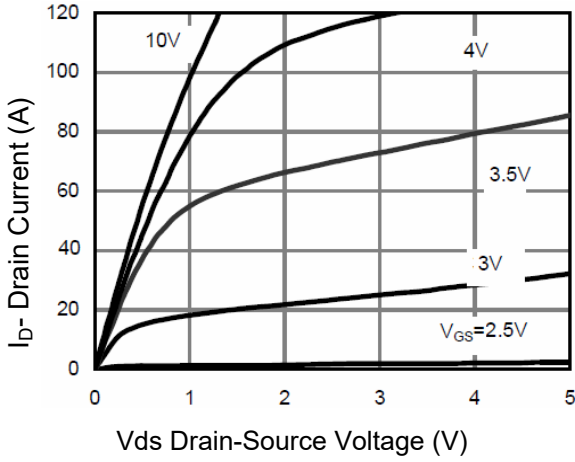


Fig. 1 Output Characteristics

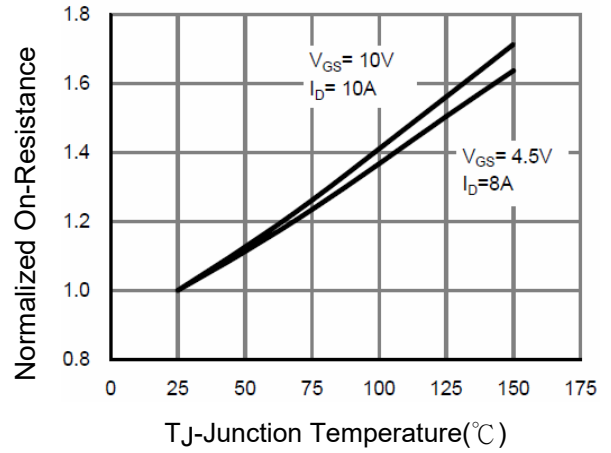


Fig. 4 Rds(on)-Junction Temperature

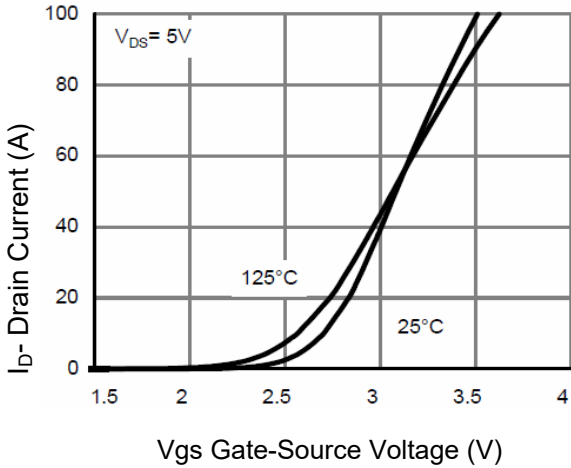


Fig. 2 Transfer Characteristics

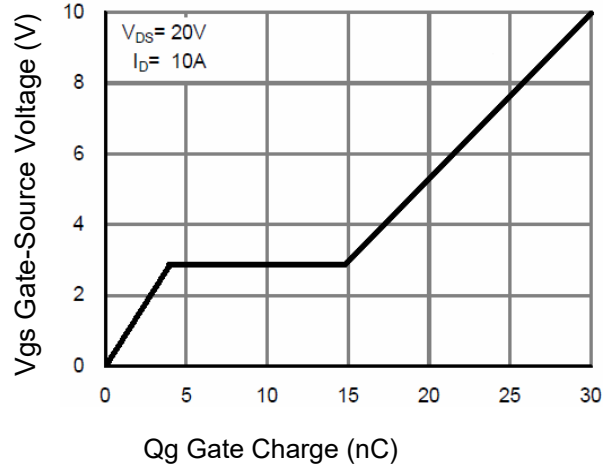


Fig. 5 Gate Charge

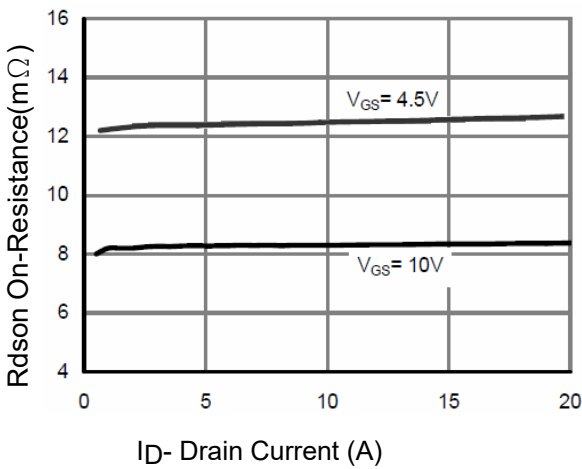


Fig. 3 Rds(on)- Drain Current

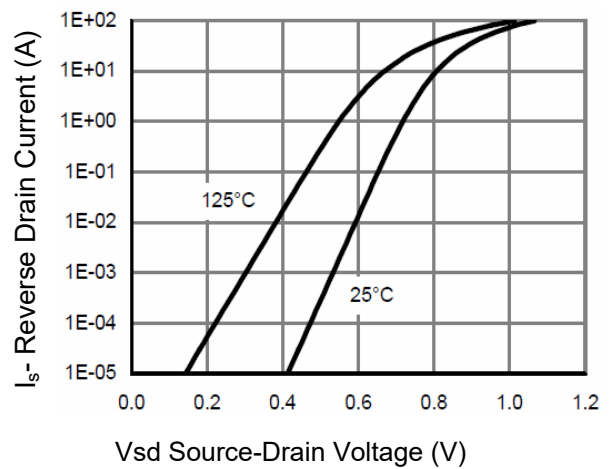


Fig. 6 Source- Drain Diode Forward

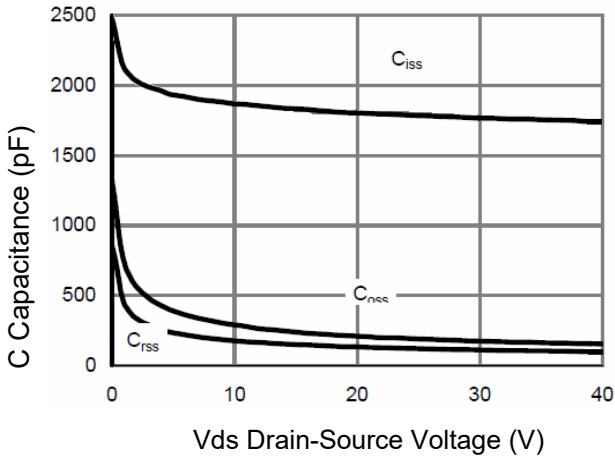


Fig.7 Capacitance vs Vds

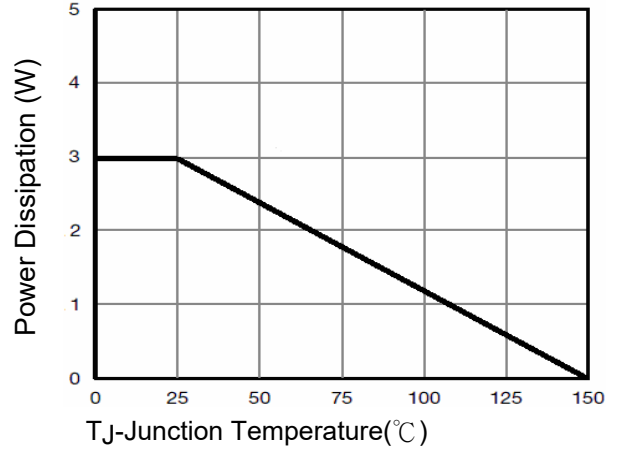


Fig. 8 BVDSS vs Junction Temperature

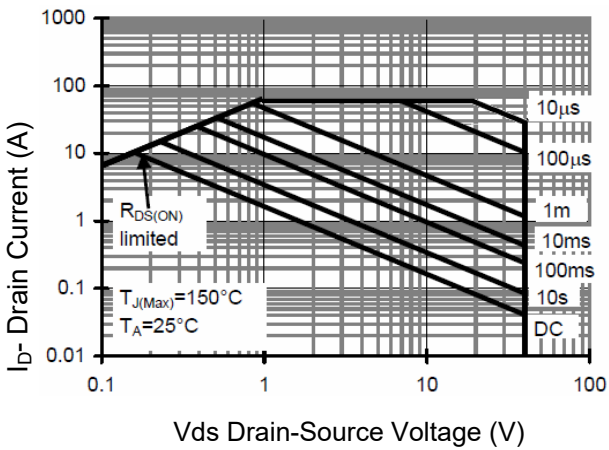


Fig.9 Safe Operation Area

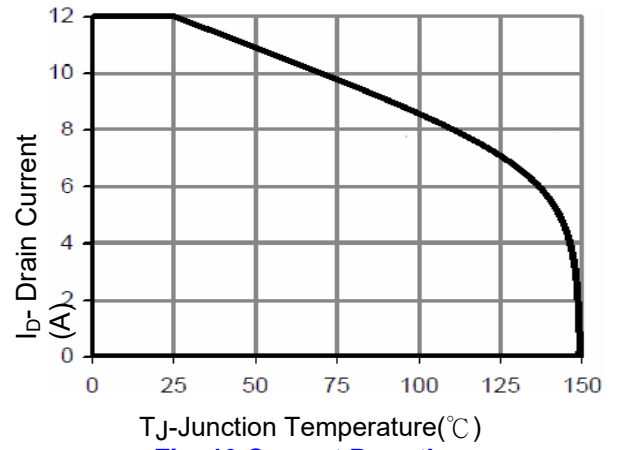


Fig. 10 Current De-rating

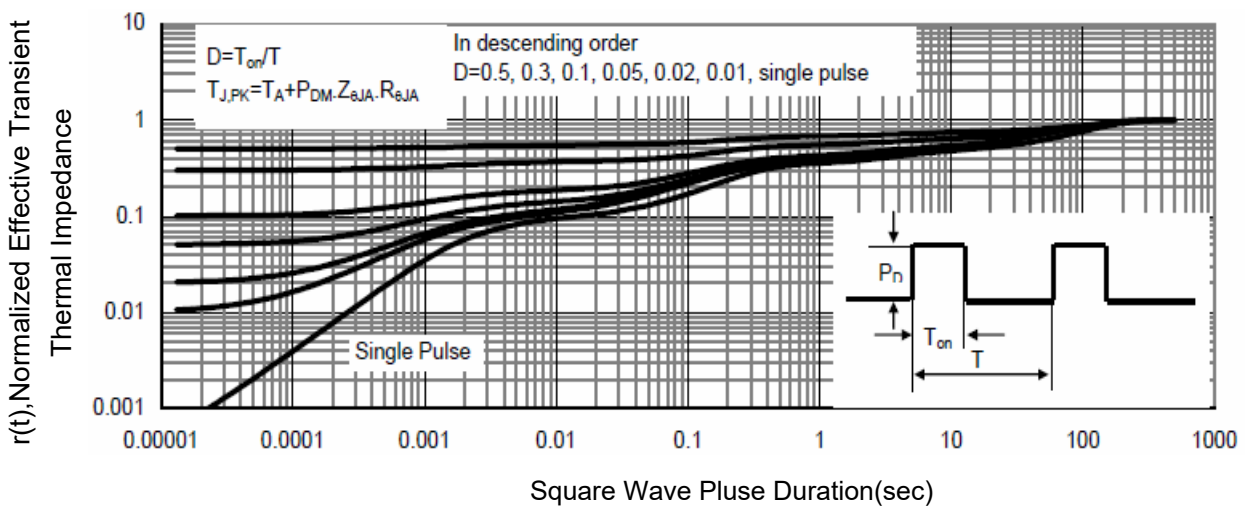


Fig.11 Normalized Maximum Transient Thermal Impedance

Disclaimer

1. Any and all Korsun Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Korsun Semiconductor representative nearest you before using any Korsun Semiconductor products described or contained herein in such applications.
2. Korsun Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Korsun Semiconductor products described or contained herein.
3. Specifications of any and all Korsun Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
4. Korsun Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
5. In the event that any or all Korsun Semiconductor products (including technical data, services described or contained herein) are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
6. No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Korsun Semiconductor CO.,LTD.
7. Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. Korsun Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
8. Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Korsun Semiconductor product that you intend to use.
9. This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.